Attorney's Docket No.: 10559-309US1 / P9630 0T05 Rec'd PCT/PT0 2 8 OCT 2007



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Gilbert Wolrich et al. Art Unit : Unknown Serial No.: 10/070,092 Examiner: Unknown

: February 27, 2002 Filed

Title : LOCAL REGISTER INSTRUCTION FOR MICRO ENGINE USED IN

MULTITHREADED PARALLEL PROCESSOR ARCHITECTURE

Commissioner for Patents Washington, D.C. 20231

REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT

Please correct the Filing Receipt for the above-referenced application to include the correct Priority data as follows:

"THIS APPLICATION IS A 371 OF PCT/US00/23993 08/31/2000 WHICH CLAIMS BENEFIT OF 60/151,961 09/01/1999"

Please supply a corrected Filing Receipt to the undersigned with respect to this application. A copy of the original Filing Receipt showing the desired changes in red ink is attached for your convenience.

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Typed or Printed Name of Person Signing Certificate Applicant : Gilbert Wolrich et al.

Serial No.: 10/070,092

Filed: February 27, 2002

Page : 2

Respectfully submitted,

Attorney's Docket No.: 10559-309US1 / P9630

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P9630

Page 1 of 2 SC H DGM

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/070,092	06/28/2002	2183	356	10559- 3091151	5	27	3

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CONFIRMATION NO. 7323

Fish & Richardson 225 Franklin Street Boston, MA 02110-2804

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Date Mailed: 08/06/2002

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Applicant(s)

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Priority Dale of 60/151,961 st	will be
DOCKETED BY PRACTICE SYSTEMS	9/1/1/
Action Code Cor. OFR Base Date 8 6 72	
Due Date 9-6-02 Deadline	
Initials PECOn	

Domestic Priority data as claimed by applicant

THIS APPLICATION IS A 371 OF PCT/US00/24054 09/01/2000 RECEIVED WHICH CLAIMS BENEFIT OF 60/151,961 02/12/12000 NOV 1 8 2002

Foreign Applications

Technology Center 2100

Projected Publication Date: Not Applicable, filed prior to November 29,2000

Non-Publication Request: No

Early Publication Request: No

Docketed By Billing Secretary					
Due Date:					
Deadline:					
Initials:					

Title

Local register instruction for micro engine used in multithreadedparallel processor architecture



712

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